

***Amendments to the Specification:***

On page 10, lines 12-13, please amend the paragraph as follows:

FIGs. 55A-D, which includes FIGs. 55A, FIGs. 55B1-55B4, FIGs. 55C1-55C3,  
and FIG. 55D, illustrates various implementation circuits for the modulator 2910  
according to embodiments of the present invention; FIGs. 55B1-55B4 should be referred  
to for all references to FIG. 55B in the specification; FIGs. 55C1-55C3 should be  
referred to for all references to FIG. 55C in the specification;

On page 11, lines 21-22, please amend the paragraph as follows:

FIG. 70A, which includes FIG. 70A1 and FIG. 70A2, illustrates an IQ receiver  
having shunt UFT modules according to embodiments of the invention; FIGs. 70A1 and  
70A2 should be referred to for all references to FIG. 70 in the specification;

On page 11, lines 27-28, please amend the paragraph as follows:

FIG. 70E, which includes FIG. 70E1 and 70E2, illustrates an example of IQ  
modulation receiver embodiment according to embodiments of the present invention;  
FIGs. 70E1 and 70E2 should be referred to for all references to FIG. 70 in the  
specification;